



Numbering	Signal Name	description
1	RXIP0	10/100 PHY Port # 0 RXIP0
2	RXIN0	10/100 PHY Port # 0 RXIN0
3	TXOP0	10/100 PHY Port # 0 TXOP0
4	TXON0	10/100 PHY Port # 0 TXON0
5	TXOP1	10/100 PHY Port # 0 TXOP1
6	TXON1	10/100 PHY Port # 0 TXON1
7	RXIP1	10/100 PHY Port # 0 RXIP1

8	RXIN1	10/100 PHY Port # 0 RXIN1
9	RXIP2	10/100 PHY Port # 0 RXIP2
10	RXIN2	10/100 PHY Port # 0 RXIN2
11	TXOP2	10/100 PHY Port # 0 TXOP2
12	TXON2	10/100 PHY Port # 0 TXON2
13	TXOP3	10/100 PHY Port # 0 TXOP3
14	TXON3	10/100 PHY Port # 0 TXON3
15	RXIP3	10/100 PHY Port # 0 RXIP3
16	RXIN3	10/100 PHY Port # 0 RXIN3
17	RXIP4	10/100 PHY Port # 0 RXIP4
18	RXIN4	10/100 PHY Port # 0 RXIN4
19	TXOP4	10/100 PHY Port # 0 TXOP4
20	TXON4	10/100 PHY Port # 0 TXON4
21	GND	Ground
22	DP	USB data pin Data +

23	DN	USB data pin Data-
24	3.3V	3.3V PowerIn
25	3.3V	3.3V PowerIn
26	GND	Ground
27	GPIO0	GPIO0
28	GND	Ground
29	RERST_N	ResetModule
30	REFCLK_0	RefClk
31	WD_RTS_N	WachDog Rst
32	PORST_N	Porst Rest
33	LINK4	ETH4 LED
34	LINK3	ETH3 LED
35	LINK2	ETH2 LED
36	LINK1	ETH1 LED
37	LINK0	ETH0 LED

38	WLED	WIFI LED
39	TXD1	UART1 TX Data
40	RXD1	UART1 RX Data
41	SDI	I2S Data input
42	SDO	I2S Data output
43	WS	I2S Channel Selection
44	I2SCLK	I2S Clock
45	I2CCLK	I2C Clock
46	I2CDATA	I2C data
47	CS1	SPI Selection
48	SPICLK	SPI Clock
49	MISO	SPI Data In
50	MOSI	SPI Data Out