



AF Power Amplifier (Split Power Supply) (200 W min, THD = 0.08%)

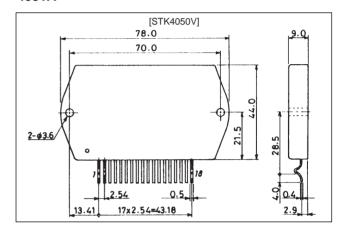
Features

- Compact packaging supports slimmer set designs
- Series designed from 20 up to 100 W (200 W) and pincompatibility (120 to 200 W have 18 pins)
- Simpler heat sink design facilitates thermal design of slim stereo sets
- \bullet Current mirror circuit application reduce distortion to 0.08~%
- Supports addition of electronic circuits for thermal shutdown and load-short protection circuit as well as pop noise muting which occurs when the power supply switch is turned on and off.

Package Dimensions

unit: mm

4051A



Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Condition	Rating	Unit
Maximum supply voltage	V _{CC} max		± 95	V
Thermal resistance	θј-с		0.95	°C/W
Junction temperature	Tj		150	°C
Operating substrate temperature	Tc		125	°C
Storage temperature	Tstg		-30 to +125	°C

Recommended Operational Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Condition	Rating	Unit
Recommended supply voltage	V _{CC}		± 66	V
Load resistance	R _L		8	Ω

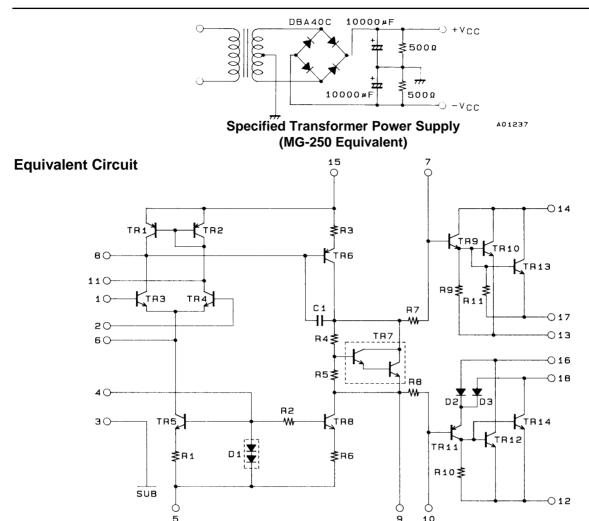
Operating Characteristics

at Ta = 25°C, V_{CC} = \pm 66 V, R_L = 8 Ω , VG = 40 dB, Rg = 600 Ω , 100 k LPF ON, R_L (non-inductive)

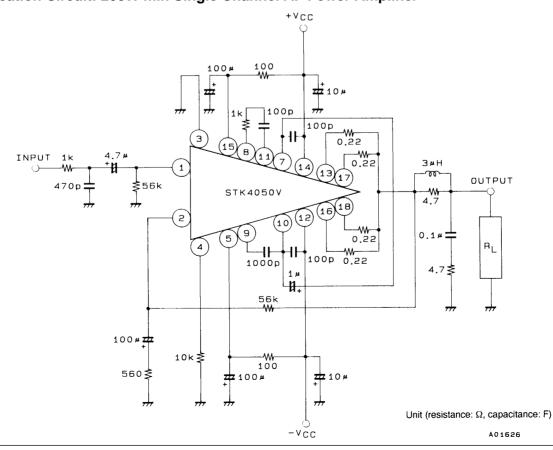
Parameter	Symbol	Condition	Rating			11.7
			min	typ	max	Unit
Quiescent current	I _{CCO}	V _{CC} = ± 80 V	15		120	mA
Output power	PO	THD = 0.08 %, f = 20 Hz to 20 kHz	200			W
Total harmonic distortion	THD	P _O = 1.0 W, f = 1 kHz			0.08	%
Frequency response	fL, fH	$P_0 = 1.0 \text{ W}, + 0 \text{ dB}$		20 to 50k		Hz
Input resistance	rį	P _O = 1.0 W, f = 1 kHz		55		kΩ
Output noise voltage	V _{NO}	$V_{CC} = \pm 80 \text{ V}, \text{ Rg} = 10 \text{ k}\Omega$			1.2	mVrms
Neutral voltage	V _N	V _{CC} = ± 80 V	-70	0	+ 70	mV

Note: Use rated power supply for test unless otherwise specified.

Output noise voltage represents the peak value on the rms scale (VTVM). The noise voltage waveform does not include the pulse noise.



Application Circuit: 200W min Single Channel AF Power Amplifier



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